

# LCM Specification

Preliminary specification

Final Specification

Project No. 项目编号	TFT-H101A4WSIFT3N40		
Customer 客户名称			
Module No. 客户型号			
Product type 产品内容	TFT LCD Module 1024 x 3RGB x 600 Dots 10.1" TFT LCD		
Signature by customer: 客户确认签章:			
<input type="checkbox"/> Trial production		<input type="checkbox"/> Mass production	
编 制	电子审核	结构审核	批 准
Y. L			

深圳市鑫洪泰电子科技有限公司

Shenzhen Hot Display Technology Co., Ltd

## 1. General Feature:

Item	Standard Value	Unit
Display Size	10.1"	--
Number of Pixels	1024(H)x3(RGB)*600(V)	--
Active Area	222.72(H) *125.28(V)	mm
Outline Dimension	235.00(H) ×143.00(V)× 3.00(T)	mm
Viewing Direction	FULL O'Clock	-
Interface	LVDS	-
Panel Driver IC	EK79001+EK73215	-
Panel Driver Condition	VCI=3.3V	V
Backlight	White LED	-
Touch Panel	Whitout Touch Panel	-
Cap Touch Driver IC	---	-
Cap Touch Driver Condition	---	V
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C



### 3. Pin Description

Pin NO.	Symbol	Description
1	VCOM	VCOM buffer input voltage
2,3	VCI	Power supply for digital circuits
4	NC	No Connect
5	RESX	Global reset pin
6	STBYB	Standby mode mode control. STBYB = "1",normal operation;STBYB = "0" ,Standby mode
7	GND	Ground
8	D0N	-LVDS differential data input
9	D0P	+LVDS differential data input
10	GND	Ground
11	D1N	-LVDS differential data input
12	D1P	+LVDS differential data input
13	GND	Ground
14	D2N	-LVDS differential data input
15	D2P	+LVDS differential data input
16	GND	Ground
17	DCKN	-LVDS differential clock input
18	DCKP	+LVDS differential clock input
19	GND	Ground
20	D3N	-LVDS differential data input
21	D3P	+LVDS differential data input
22	GND	Ground
23,24	NC	No Connect
25	GND	Ground
26,27	NC	No Connect
28	HSD	6bot/8bit mode select , L=8 BIT , H=6BIT
29	AVDD	Power supply for analog circuits
30	GND	Ground
31,32	LED_K	LED Cathode
33	L/R	Horizontal inversion
34	U/D	Vertical inversion
35	VGL	Gate OFF Voltage
36,37	NC	No Connect
38	VGH	Gate ON Voltage
39,40	LED_A	LED Anode(+9.6V/240mA)
-END-		